

CURRICULUM VITAE



RAJEEV KUMAR

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Objective:

Working in real time projects in VLSI Design so that I am finally stood in parallel with latest technology.

Educational Qualification:

Degree	University/Institution	Year	Percentage
M.Tech Microelectronics & VLSI Design	Kurukhsetra University	2011	71.9
MSc Electronic Science	Kurukhsetra University	2007	63.6
BSc Electronics	Mukand Lal National College	2005	64.6

VLSI Design:

Field	Tool Used
Full Custom Design	IC Station(Mentor Graphics)
FPGA based Prototyping	Modelsim Altera 6.4a,Xilinx ISE 10.1
Embedded System Design	Keil μ Vision 4 Software
Microprocessor Programming	8085 Simulator

Projects Done:

Project Title	Degree
Design of Reconfigurable VLIW Processor	M.Tech Microelectronics & VLSI Design
Process Optimization for Photolithography	MSc Electronic Science

System Platform:

Windows: XP, 7

MS Office

Languages Known:

Verilog HDL

Assembly language of 8085 Microprocessor

Assembly language of 8051 Microcontroller

Experience:

Institute/University	Designation	Year
Guru Nanak Khalsa College	Lecturer	2007-2009
IITT College of Engg.	Coordinator M.Tech ECE	2011-2013
Indus International University	Assistant Professor	2013-2018

Personal Details:

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Director/Principal Kurukhsetra Institute of Technology & Management, Kurukhsetra

Dr.Satinder Sharma

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Rajeev Kumar